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Revisions

Rev	Description	Date	Approved
A	Proto Release	23-FEB-11	J.H.
B	Proto Update	13-APR-11	J.H.
C	Proto Update	26-APR-11	J.H.

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Designer: Jay Hartvigsen		Drawing Title: TWR-MCF51JF	
Drawn by: Jay Hartvigsen		Page Title: Table of Contents/Revisions	
Approved: Marilyn Hubbard	Size: C	Document Number: SCH-26990 PDF: SPF-26990	Rev: C
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1. Unless Otherwise Specified:

- All resistors are in ohms
- All capacitors are in uF
- All voltages are DC
- All polarized capacitors are aluminum electrolytic

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Device type number is for reference only. The number varies with the manufacturer.


4. Special signal usage:

- _B Denotes - Active-Low Signal
- <> or [] Denotes - Vectored Signals

5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

Power & Ground Nets

NET	VOLTAGE	DESCRIPTION
P5V_USB	5V	Primary input power. Filtered from USB connector. Input to USB power switch.
P5V_SW	5V	Output of USB power switch controlled by the 5V_EN signal from the JM60 MCU. Used by OSBDM voltage translation circuits.
P5V_TRG_USB	5V	Output of USB power switch controlled by the VTRG_EN signal from the JM60 MCU. Goes to regulator input select header.
USB0_VBUS	5V	USB power from primary elevator.
P5V_JF_USB	5V	Secondary input power. Filtered from JF micro AB USB connector. Goes to regulator input select header
P5V_ELEV	5V	Power from the elevator boards.
P3V3	3.3V	Output of 3.3V regulator. May also be supplied externally by connecting to the board voltage select header at pins 1 and 4.
V_BRD	1.8-3.3V	Output of 1.8v or 3.3V 3 regulators as selected by the board voltage select header. May also be supplied externally by connecting to the board voltage select header at pins 3 and 4.
VREG_IN	5V	Power into the on board voltage regulators.
MCU_PWR	1.8-3.3V	MCU digital power. Filtered from V_BRD
VDDA	1.8-3.3V	VDDA power for MCU and analog circuits. Filtered from MCU_PWR.
VREFH	1.8-3.3V	Upper reference voltage for ADC on the MCU. Filtered from VDDA.
VREFL	0V	Lower reference voltage for ADC on the MCU. Filtered from VSSA.
VSSA	0V	VSSA power for MCU and analog circuits. Filtered from GND.
GND	0V	Digital Ground.

		
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ELEVATOR CONNECTORS

Sheet 9

Sheet 6

OSBDM/USB Bridge Circuit
 USB Mini B Connector
 MC9S08JM60
 Voltage Translation
 OSBDM AND EZPORT Header
 Power Supply Circuits

Sheets 4 and 5

MCF51JF128 MCU
 8 MHz XTAL
 VSSA/VDDA filter
 VREFH/VREFL filter
 VREF_OUT
 VREGIN, VOUT33

Sheet 7

INFRARED PORT

Sheet 7

PUSH BUTTON
 SAI HEADER
 AUDIO OUTPUT JACK
 JF USB MICRO AB
 CONNECTOR WITH
 POWER SWITCH

Sheet 8

CAPACITIVE TOUCH PADS
 WITH LEDs

Sheet 8


TOWER PLUG-IN (TWRPI)
 GENERAL PURPOSE HEADERS

Sheet 8

ANALOG INPUTS
 MMA7660 ACCELEROMETER
 POTENTIOMETER
 MICROPHONE

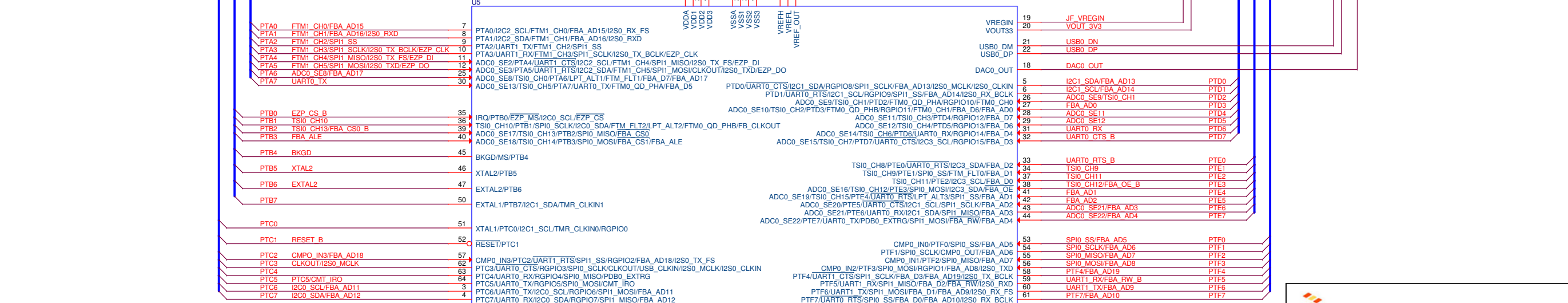
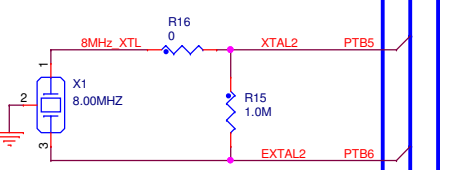
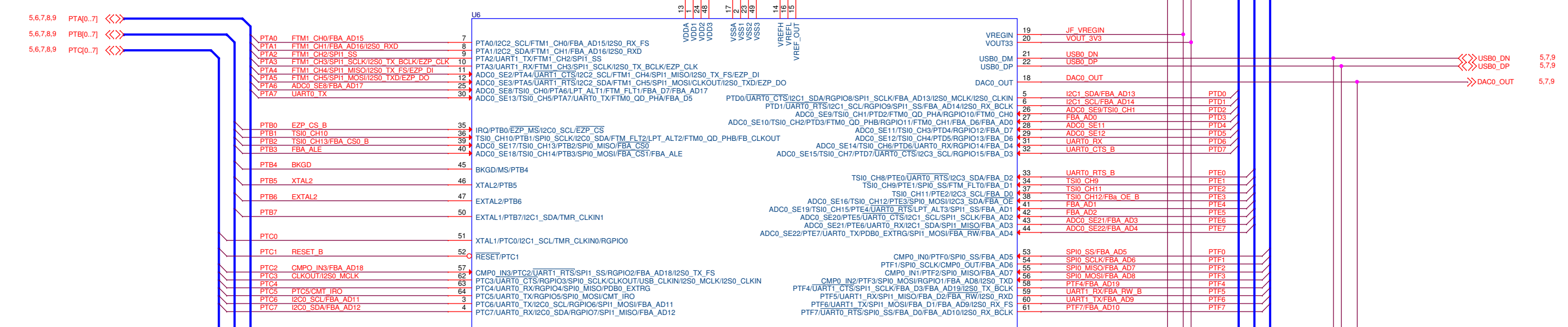
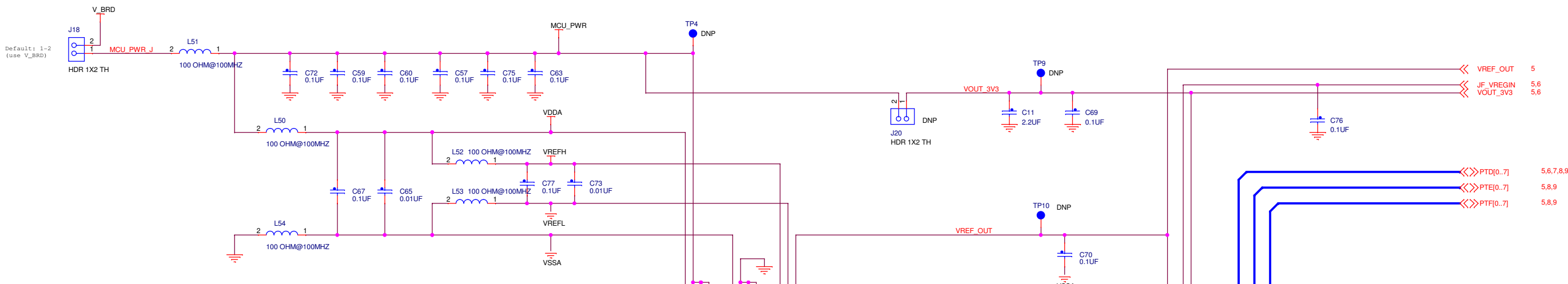
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TOWER PLUG-IN (TWRPI)
 TOUCH HEADER



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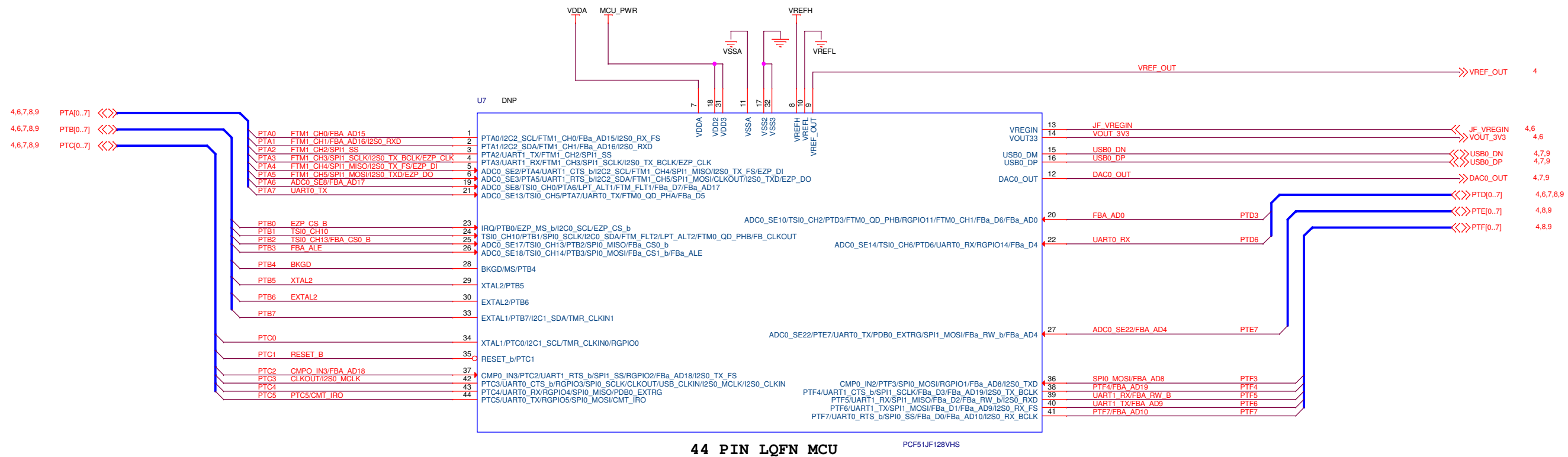


64 PIN LQFP MCU IN A SOCKET

THE 64 PIN MCU, THE 64 PIN SOCKET, AND THE 44 PIN MCU ON THE NEXT PAGE ARE ALL WIRED IN PARALLEL. THEY ARE CONCENTRICALLY PLACED ON THE BOARD AND ONLY ONE IS USED DEPENDING ON THE ASSEMBLY.

ICAP Classification: FCP: _____ FIJU: X PUBI: _____
 Drawing Title: **TWR-MCF51JF**
 Page Title: **MCF51JF 64pin MCU & SKT**

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44 PIN LQFN MCU PCF51JF128VHS

THE 44 PIN MCU ON THIS PAGE AND THE 64 PIN MCU AND 64 PIN SOCKET ON THE PREVIOUS PAGE ARE ALL WIRED IN PARALLEL.

THEY ARE CONCENTRICALLY PLACED ON THE BOARD AND ONLY ONE IS USED DEPENDING ON THE ASSEMBLY.

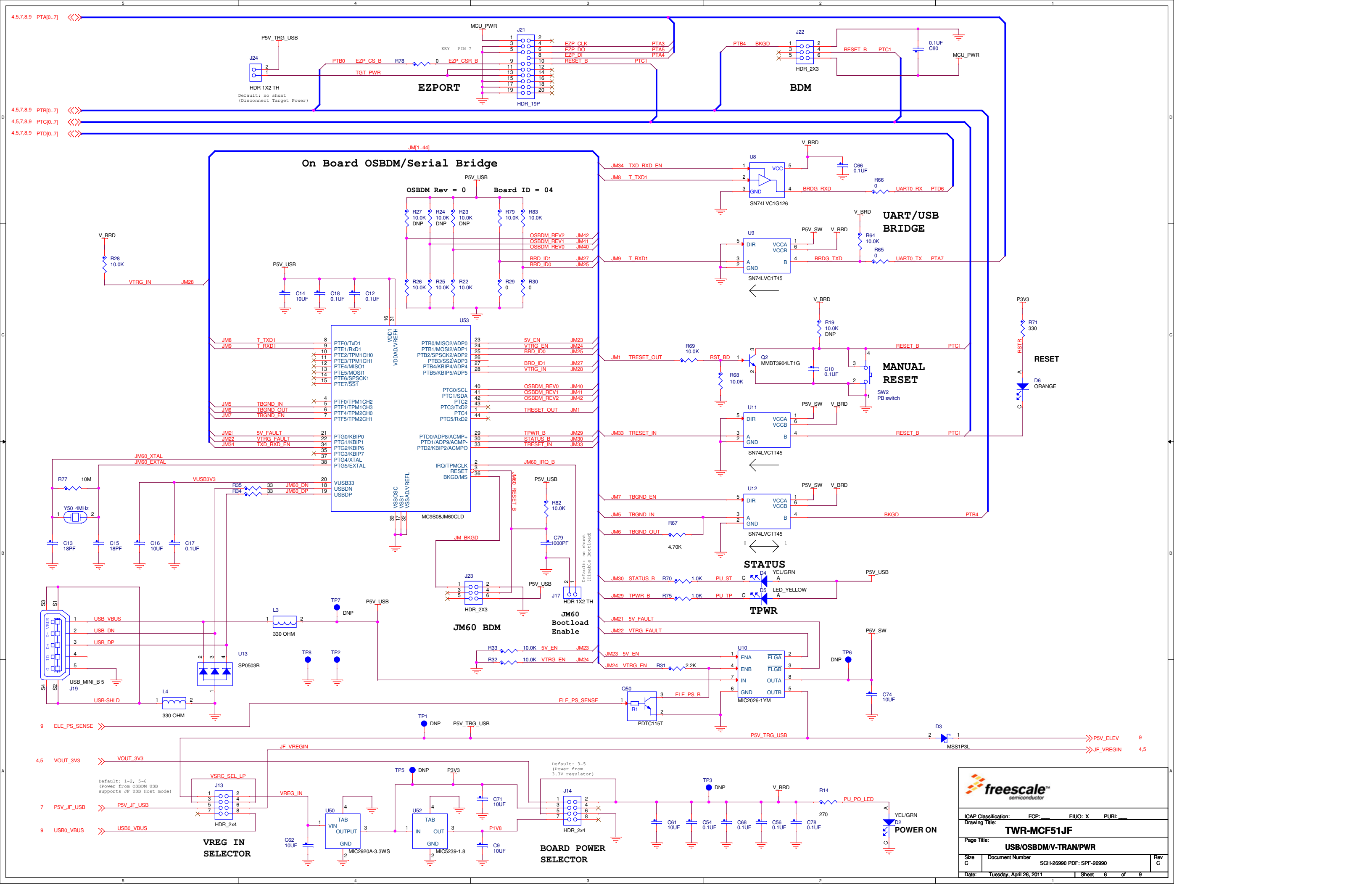
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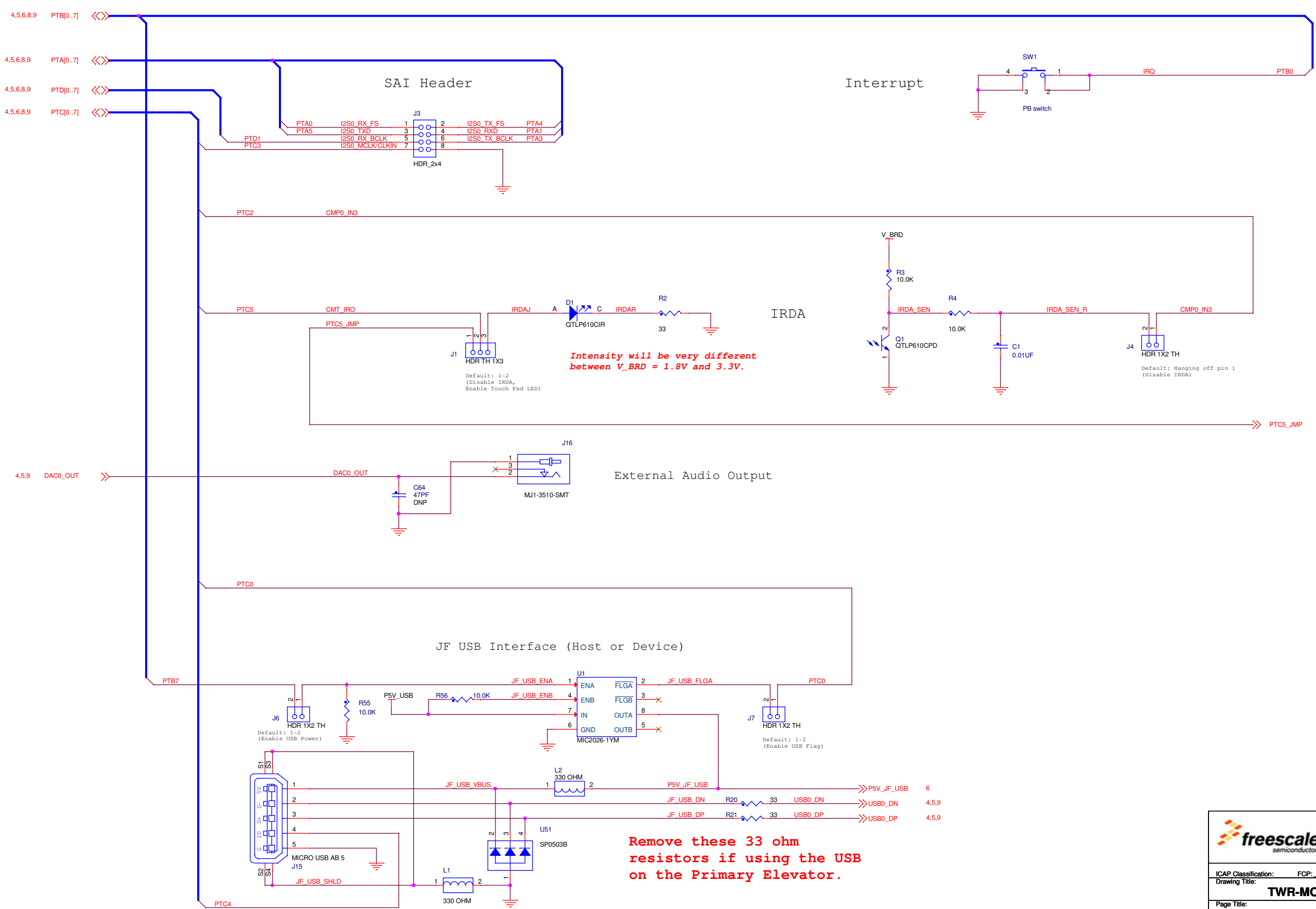
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Page Title: **MCF51JF 44pin MCU**

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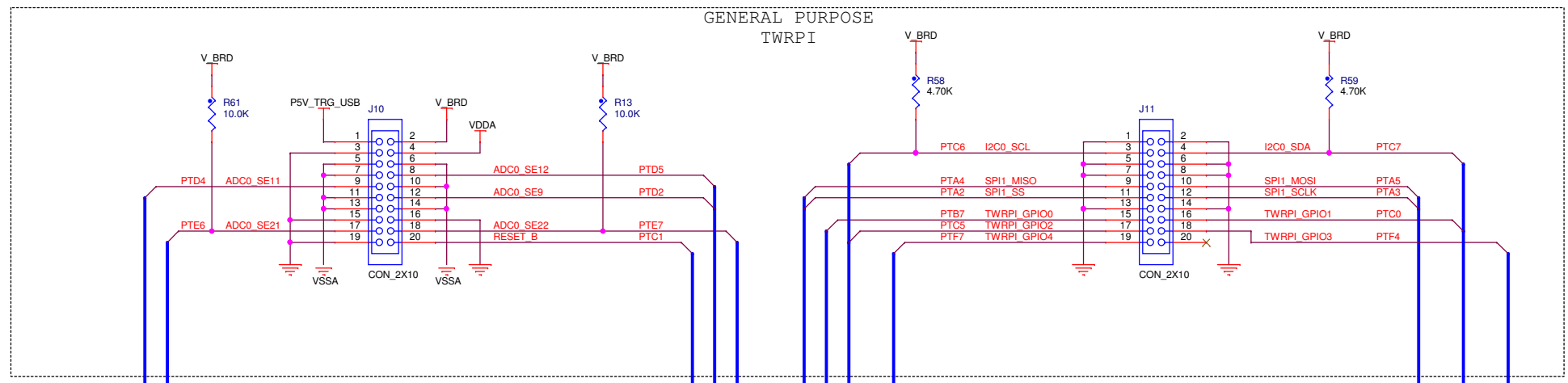
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Intensity will be very different between V_BRD = 1.8V and 3.3V.

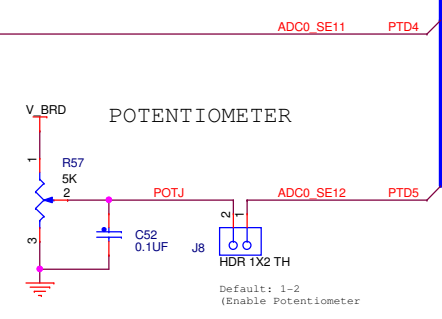
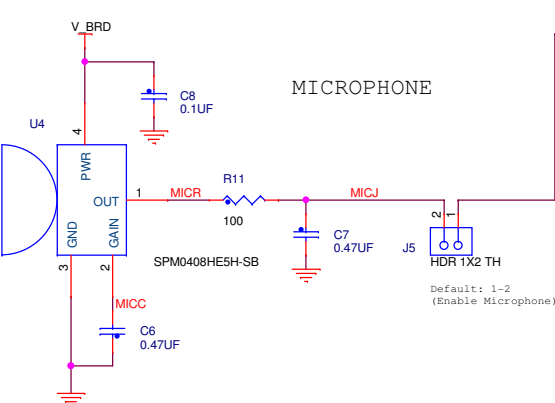
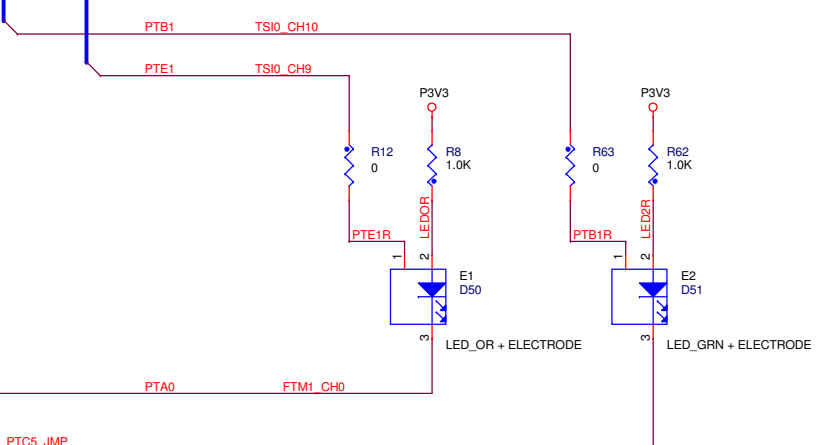
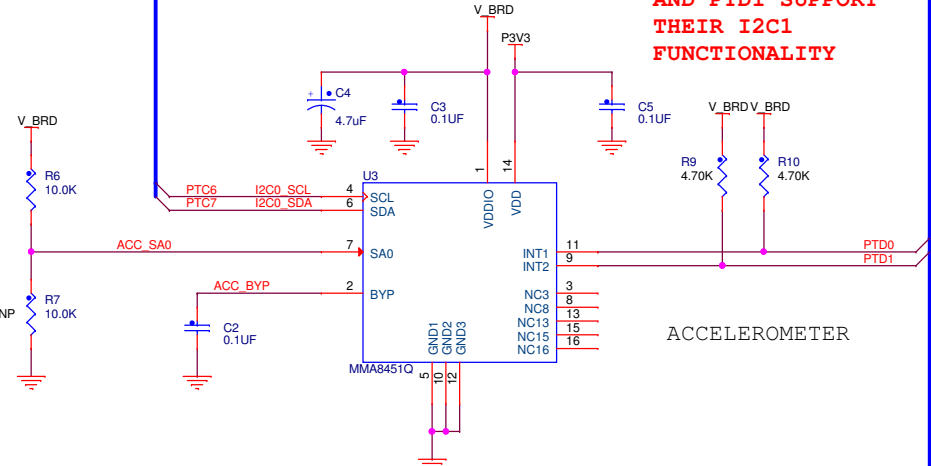
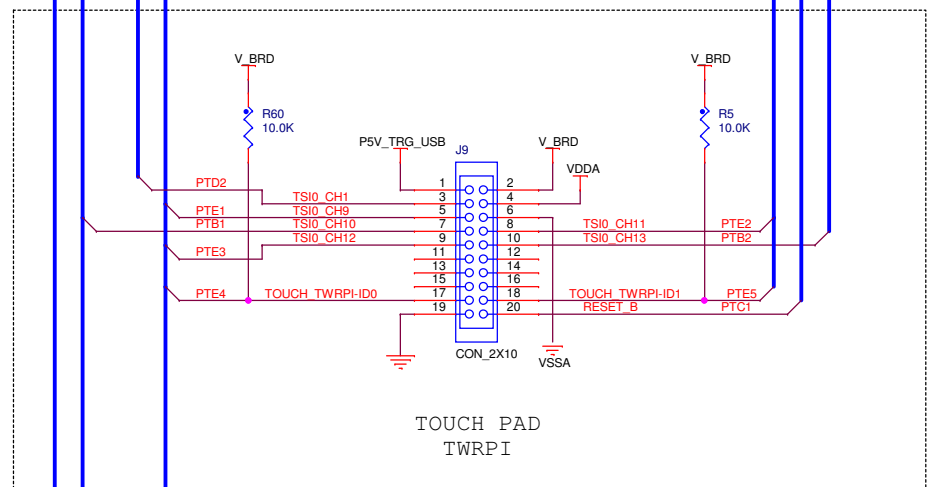
Remove these 33 ohm resistors if using the USB on the Primary Elevator.

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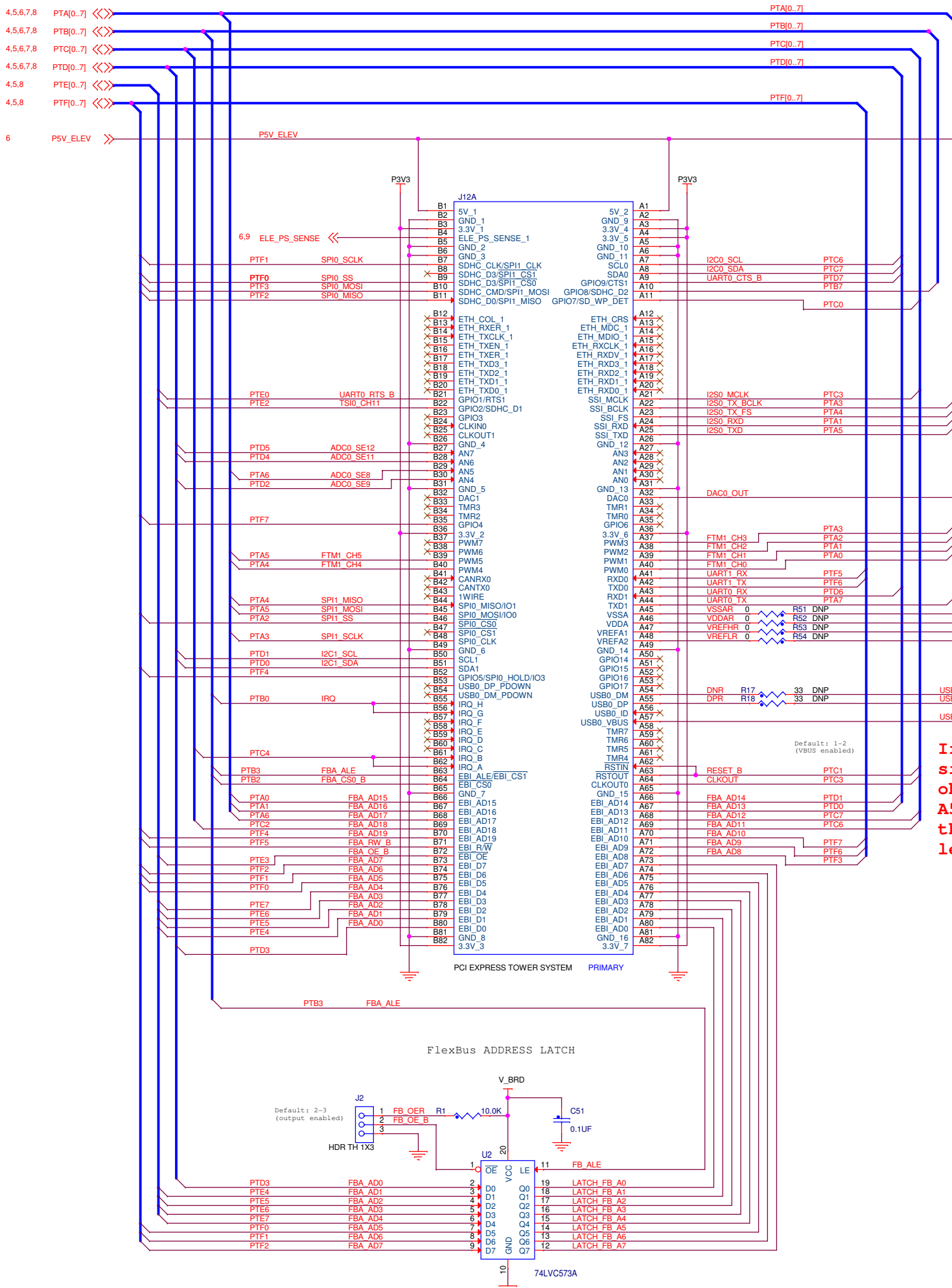
- 4,5,6,7,9 PTA[0..7] <<>
- 4,5,6,7,9 PTB[0..7] <<>
- 4,5,6,7,9 PTC[0..7] <<>
- 4,5,6,7,9 PTD[0..7] <<>
- 4,5,9 PTE[0..7] <<>
- 4,5,9 PTF[0..7] <<>

NOTE: THE PULLUP RESISTORS ON PTD0 AND PTD1 SUPPORT THEIR I2C1 FUNCTIONALITY



PTC5_JMP <<>

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If using the Tower USB signals install the 33 ohm resistors on pins A54 and A55. If using the micro USB connector leave these resistors off.

6.9 ELE_PS_SENSE

USB0_DN 4.5,7
 USB0_DP 4.5,7
 USB0_VBUS 6

