

# FDBS09H04A\_F085A/FDPS09H04A\_F085A Smart High Side Switch

## Features

- Short circuit protection with latch
- Current limitation
- Overload protection
- Thermal shutdown with restart
- Overvoltage protection (including load dump)
- Loss of ground protection
- Loss of supply protection (with external diode for charged inductive load)
- Very low standby current
- Fast demagnetization of inductive loads
- ESD protection
- Optimized static electromagnetic compatibility
- Diagnostic function - Proportional load current sense (with defined fault signal in case of overload operation, over temperature shutdown and/or short circuit shutdown)
- Qualified to AEC

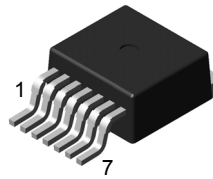
## Typical Applications

- Power switch with current sense diagnostic feedback for DC grounded loads
- All types of resistive, inductive, and capacitive loads
- Replace electromechanical relays, fuses and discrete circuits

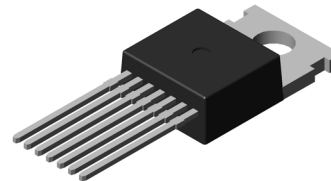
## Description

N channel power FET with charge pump, current controlled input and diagnostic feedback with load current sense, integrated in Smart Trench chip on chip technology. Provides embedded protective functions.


### TO263-7L



### TO220-7L



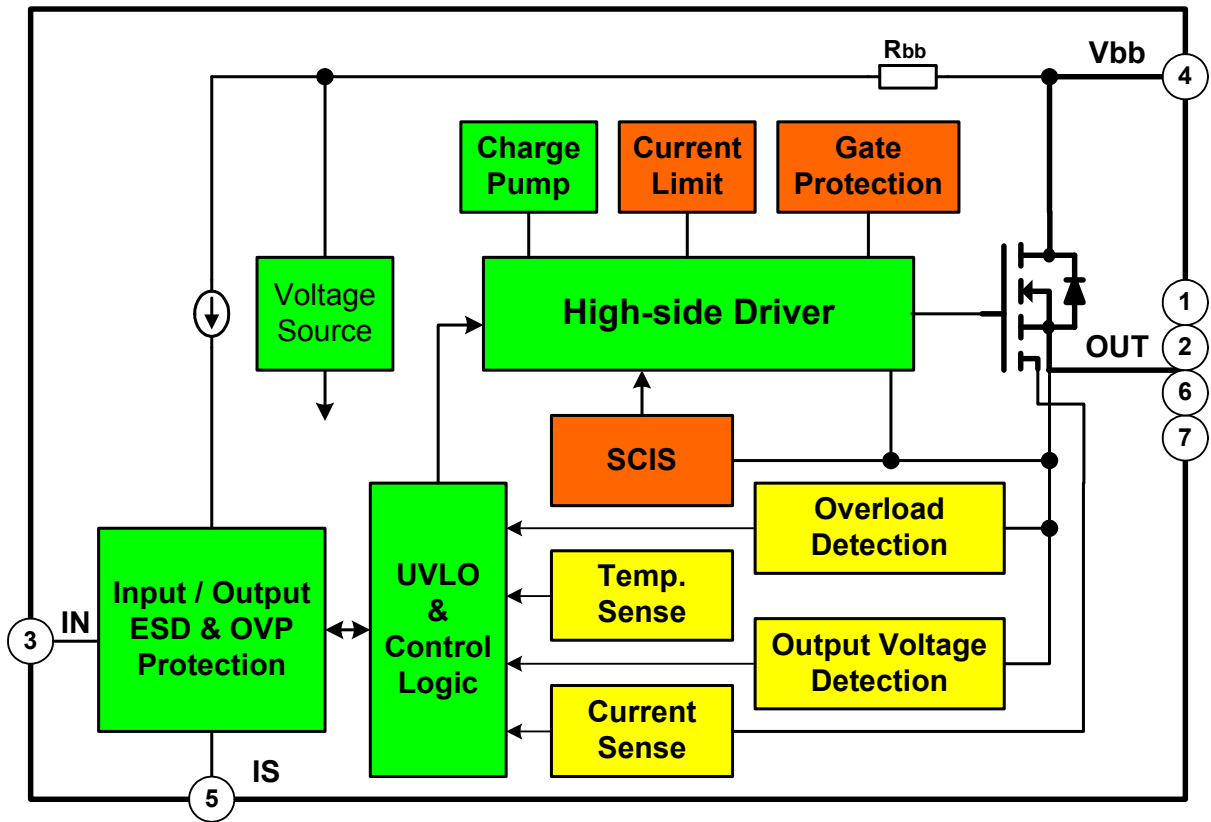
## Ordering Information

Part Number	Package	Operating Temperature	 Eco Status	Packing Method
FDBS09H04A_F085A	TO263-7L	-40 °C - 150 °C	RoHS	Tape & Reel



For Fairchild's definition of "green" Eco Status, please visit: [http://www.fairchildsemi.com/company/green/rohs\\_green.html](http://www.fairchildsemi.com/company/green/rohs_green.html).

## Block Diagram



## Pin Definitions

Pin Number	Pin Name	I/O	Pin Function Description
1	OUT	A	Output to loads; Pins 1, 2, 6 and 7 must be externally shorted
2	OUT	A	Output to loads; Pins 1, 2, 6 and 7 must be externally shorted
3	IN	A	Input; Activates the power switch if shorted to ground
4	Vbb	P	Supply Voltage; Pin 4 and tab are internally shorted
5	IS	A	Sense Output, Diagnostic feedback; Provides at normal operation a sense current proportional to the load current; in case of overload, over temperature and/or short circuit a defined current is provided
6	OUT	A	Output to loads; Pins 1, 2, 6 and 7 must be externally shorted
7	OUT	A	Output to loads; Pins 1, 2, 6 and 7 must be externally shorted

## Absolute Maximum Ratings

At Tj=25°C unless otherwise specified.

Parameter	Symbol	Values	Unit	
Supply voltage	Vbb	38	V	
Supply voltage for full short circuit protection <sup>1)</sup>	Vbb	30	V	
Load dump protection VLoadDump = UA + VS, UA=13.5V Ri=2Ω, RL=1Ω, td=400ms, IN=Low or High	VLoadDump <sup>2)</sup>	45	V	
Load current (short-circuit current)	IL	Self-limited	A	
Operating temperature range	Tj	-40 - 150	°C	
Storage temperature range	Tstg	-55 - 150	°C	
Power Dissipation (DC)	Ptot	81	W	
Inductive load switch-off energy dissipation <sup>3)</sup> Single pulse, IL=12.5A, L=5mH, Vbb=12V, Tj=150°C	EAS	388	mJ	
Electrostatic discharge capability (ESD) (Human Body Model)	IS	VESD	2	KV
	IN	VESD	2	KV
	VBB, Output	VESD	5	KV
Current through input pin (DC)	IIN	+15, -120	mA	
Current through current sense pin (DC)	IS	+15, -120	mA	
Input voltage slew rate Vbb <= 16V Input voltage slew rate Vbb > 16V <sup>4)</sup>	dVbIN / dt	self-limited 20	V/us	

Notes:

- 1) Short circuit is defined as a combination of remaining resistances and inductances. See schematic on page 11.
- 2) VLoad dump is setup without the DUT connected to the generator.
- 3) See also diagram on page 11.
- 4) See also on page 7. Slew rate limitation can be achieved by means of using a series resistor RIN in the input path. This resistor is also required for reverse operation. See also page 10.

## Electrical Characteristics

At Tj=25°C, Vbb=12V unless otherwise specified.

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>Thermal Characteristics</b>						
Thermal resistance	RthJC <sup>5)</sup>	(junction to case)	-	0.6	0.8	K/W
	RthJA	(junction to ambient)	-	70	-	
		device on PCB <sup>6)</sup> , SMD version only	-	35	45	
<b>Load Switching Capability and Characteristics</b>						
On-state resistance (pin 3 to pins 1, 2, 6, 7)	RON	VIN=0, Vbb=5.5V, IL=10A, Tj=25°C	-	5.5	9	mΩ
		VIN=0, Vbb=5.5V, IL=10A, Tj=150°C	-	10	15	
		VIN=0, Vbb=12V, IL=10A, Tj=25°C	-	5.5	9	
		VIN=0, Vbb=12V, IL=10A, Tj=150°C	-	10	15	
Output voltage drop limitation at small load currents (tab to pins 1, 2, 6, 7)	VON(NL)	Tj=-40 - 150°C	-	35	65	mV
Nominal load current (tab to pins 1, 5)	IL(ISO)	ISO Proposal <sup>7)</sup> : VON<=0.5V, Tc=85°C, Tj<=150°C	38	48	-	A
	IL(NOM)	SMD <sup>6)</sup> <sup>7)</sup> : VON<= 0.5V, Ta=85°C, Tj<=150°C	12	14	-	
Turn-on time (to 90% VOUT)	ton	RL=2.2Ω, Tj = -40 - 150°C	-	180	400	us
Turn-off time (to 10% VOUT)	toff		-	150	500	
Slew rate on (25% to 50% VOUT)	dV / dton	RL=2.2Ω, Tj = -40 - 150°C	-	0.2	0.45	V/us
Slew rate off (50% to 25% VOUT)	-dV / dtoff	RL=2.2Ω, Tj = -40 - 150°C	-	0.2	0.55	V/us
<b>Operating Parameters</b>						
Operating Voltage (VIN=0)	Vbb(ON)	Tj = -40 - 150°C	5.5		38	V
Under voltage shutdown <sup>8)</sup>	Vbb(u)		-	1.5	3.5	V
Under voltage restart of charge pump	Vbb(ucp)			3.7	5.5	V
Over voltage protection <sup>9)</sup>	VZ,IN	Ibb=15mA, Tj = -40 - 150°C	42.5	47.3	-	V
Standby current	Ibb(off)	IIN=0, Tj = -40 - 120°C	-	0.8	5.3	uA
		IIN=0, Tj = 150°C	-	8	20	
<b>Reverse Battery <sup>10)</sup></b>						
Reverse battery voltage	-Vbb	IL=-10A, RIS=1KΩ	-	-	18	V
On-state resistance (pin 4, tab to pins 1, 2, 6, 7) <sup>8)</sup>	RON(REV)	Vbb=-8V, VIN=0, IL=-10A, RIS=1KΩ, Tj=25°C	-	6.1	13	mΩ
		Vbb=-8V, VIN=0, IL=-10A, RIS=1KΩ, Tj=150°C	-	13	18	
		Vbb=-12V, VIN=0, IL=-10A, RIS=1KΩ, Tj=25°C	-	6.1	11.5	
		Vbb=-12V, VIN=0, IL=-10A, RIS=1KΩ, Tj=150°C	-	10	17	
Integrated resistor in Vbb line	Rbb	Is=1mA, VIN=5V @ Tj =125°C	65	85	110	Ω
<b>Inverse operation <sup>11)</sup></b>						
Output voltage drop (pin 4, tab to pins 1, 2, 6, 7) <sup>8)</sup>	-VON(inv)	IL=-10A, RIS=1KΩ, Tj=25°C	-	800	-	mV
		IL=-10A, RIS=1KΩ, Tj=150°C	-	600	-	
Turn-on delay after inverse operation	td(inv)	IL> 0A <sup>8)</sup> VIN(inv)=VIN(fwd)=0V	-	1	-	ms

Notes:

5) Thermal resistance RthCH case to heatsink (about 0.5... 0.9 K/W with silicone paste) not included!

6) Device on 76.2mm \* 114mm \* 1.57mm glass epoxy PCB. Still air conditions.

7) Not subject to production test, Parameters are calculated from Ron and Rthjc or Rthja.

8) Not subject to production test, specified by design.

9) See also VON(CL) in circuit diagram page 8.

10) For operation at voltages higher than |16V| please see required schematic on page 9.

11) Permanent Inverse operation results eventually in a current flow via the intrinsic diode of the power DMOS. In this case the device switches on with a time delay td(inv) after the transition from inverse to forward mode.

## Electrical Characteristics

At Tj=25°C, Vbb=12V unless otherwise specified

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>Protection Functions</b> <sup>12)</sup>						
Short circuit current limit (pin 4, tab to pins 1, 2, 6, 7) at VON=6V <sup>13) 14)</sup>	IL6(SC)	Tj=-40°C Tj=25°C Tj=150°C	- - 85	130 120 110	160 - -	A
Short circuit current limit (pin 4, tab to pins 1, 2, 6, 7) at VON=12V <sup>13)</sup>	IL12(SC)	Tj=-40°C Tj=25°C Tj=150°C	- - 55	93 85 73	125 - -	A
Short circuit current limit (pin 4, tab to pins 1, 2, 6, 7) at VON=18V <sup>13) 14)</sup>	IL18(SC)	Tj=-40°C Tj=25°C Tj=150°C	- - 41	75 68 61	100 - -	A
Short circuit current limit (pin 4, tab to pins 1, 2, 6, 7) at VON=24V <sup>13)</sup>	IL24(SC)	Tj=-40°C Tj=25°C Tj=150°C	- - 20	53 47 41	75 - -	A
Short circuit current limit (pin 4, tab to pins 1, 2, 6, 7) at VON=30V <sup>13)</sup>	IL30(SC)	Tj=-40°C Tj=25°C Tj=150°C	- - 18	34 31 28	50 -- -	A
Short circuit shutdown detection voltage <sup>14)</sup>	VON(SC)		2.5	3.5	4.5	V
Short circuit shutdown delay after input current positive slope Min. value valid only if "off-signal" time exceeds 30us	td(SC1)	VON > VON(SC), Tj=-40 ~ 150°C	370	520	700	us
Output clamp(inductive load switch off) at VOUT= Vbb-VON(CL)(overvoltage) <sup>15)</sup>	VON(CL)	IL=40mA	38.5	41.5	-	V
Thermal overload trip temperature <sup>14)</sup>	Tjt		165	178	-	°C
Thermal hysteresis <sup>14)</sup>	ΔTjt		-	10	-	K

Notes:

12) Integrated protection functions are designed to prevent IC destruction under fault conditions described in the data sheet. Fault conditions are considered as "outside" normal operating range. Protection functions are not designed for continuous repetitive operation.

13) Short circuit current limit for max. duration of td(SC1), prior to shutdown, see also Figures 3.x on page 14 and 15.

14) Not subject to production test, specified by design.

15) See also Figure 2b on page 14.

## Electrical Characteristics

At T<sub>j</sub>=25°C, V<sub>bb</sub>=12V unless otherwise specified.

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>Diagnostic Characteristics</b>						
Current sense ratio, static on-condition KILIS = IL / IIS < IIS <sub>min</sub> <sup>16)</sup> VIS < VOUT-5V, V <sub>bin</sub> > 4.5V  IIN=0 (e.g. during deenergizing of inductive loads)	KILIS	IL=35A, T <sub>j</sub> =-40°C	-	13200	-	-
		IL=35A, T <sub>j</sub> =25°C	11000	13200	15000	
		IL=35A, T <sub>j</sub> =125°C	11000	13275	15000	
		IL=15A, T <sub>j</sub> =-40°C	11000	13250	15000	
		IL=15A, T <sub>j</sub> =25°C	9300	13000	18000	
		IL=15A, T <sub>j</sub> =125°C	10500	13000	17000	
		IL=15A, T <sub>j</sub> =-40°C	11000	12900	16000	
		IL=2.5A, T <sub>j</sub> =-40°C	7800	13100	18200	
		IL=2.5A, T <sub>j</sub> =25°C	8200	13400	18500	
		IL=2.5A, T <sub>j</sub> =125°C	8000	13450	18850	
		IL=0.5A, T <sub>j</sub> =-40°C	5800	12500	17900	
		IL=0.5A, T <sub>j</sub> =25°C	7700	13000	18500	
		IL=0.5A, T <sub>j</sub> =125°C	8100	13700	19200	
Sense current under fault conditions <sup>17)</sup>	IIS <sub>fault</sub>	VON > 1V, typ T <sub>j</sub> = -40 - 150°C	3.5	5.2	7.5	mA
Sense saturation current	IIS <sub>lim</sub>	VON < 1V, typ T <sub>j</sub> = -40 - 150°C	3.5	5.8	8.5	mA
Fault-sense signal delay after input current positive slope	tdelay(fault)	VON > 1V, typ T <sub>j</sub> = -40 - 150°C	350	500	650	us
Current sense leakage current	IIS(LL)	IIN = 0	-	0	0.5	uA
Current sense offset current	IIS(LH)	VIN = 0, IL <= 0	-	0	1	uA
Minimum load current for sense functionality	IL(MIN)	VIN = 0, T <sub>j</sub> = -40 - 150°C	70	-	-	mA
Current sense settling time to IIS <sub>static</sub> after input current positive slope <sup>18)</sup>	tson(IS)	IL = 0 --> 20A, T <sub>j</sub> = -40 - 150°C	-	300	650	us
Current sense settling time during on condition <sup>18)</sup>	tslc(IS)	IL = 10 --> 20A, T <sub>j</sub> =-40 ~ 150°C	-	50	100	us
Overvoltage protection	VZ,IS	I <sub>bb</sub> = 15mA, T <sub>j</sub> = -40 - 150°C	42.5	47.3	-	V
<b>Input</b>						
Required current capability of input switch	IIN(on)	T <sub>j</sub> = -40 - 150°C	-	1.5	3.0	mA
Input current for turn-off	IIN(off)	T <sub>j</sub> = -40 - 150°C	-	-	15	uA

Notes:

16) See also Figures 4.x and 6.x on page 15 and 16.

17) Fault conditions are overload during on (i.e. VON>1V typ.), over temperature and short circuit; see also truth table on page 7.

18) Not subject to production test, specified by design.

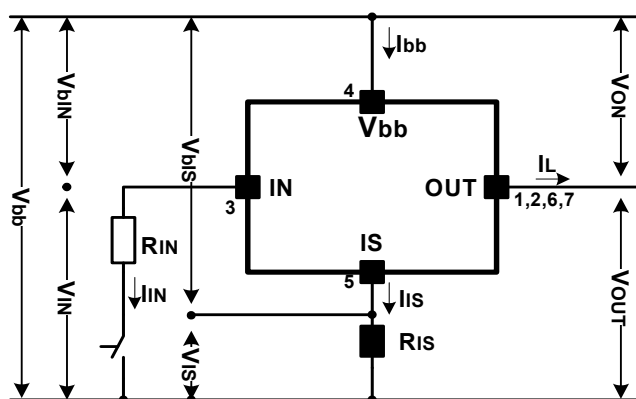
## Application Information

### 1. Truth Table

Sense current under fault conditions	Input Current Level	Output Level	Current Sense
Normal operation	L	L	= 0 (IIS(LL))
	H	H	nominal
Overload <sup>19)</sup>	L	L	= 0 (IIS(LL))
	H	H	IIS,fault
Short circuit to GND <sup>20)</sup>	L	L	= 0 (IIS(LL))
	H	L	IIS,fault
Over temperature	L	L	= 0 (IIS(LL))
	H	L	IIS,fault
Short circuit to Vbb	L	H	= 0 (IIS(LL))
	H	H	< nominal <sup>21)</sup>
Open load	L	Z	= 0 (IIS(LL))
	H	H	= 0 (IIS(LH))

L = " Low " Level, Z = High impedance, potential depends on external circuit, H = "HIGH" Level

### 2.Terms



Notes:

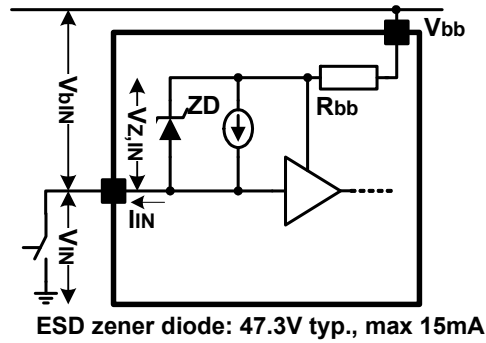
19) Overload is detected at the following condition:  $1V (typ.) < VON < 3.5V (typ.)$ . See also page 10.

20) Short Circuit is detected at the following condition:  $VON > 3.5V (typ.)$ . See also page 11.

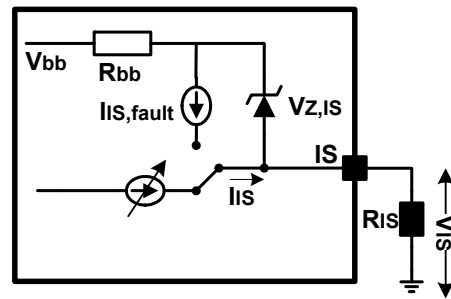
21) Low ohmic short to Vbb may reduce the output current  $I_L$  and therefore also the sense current  $IIS$ .

### 3. Detailed Function Blocks

#### 3.1 Input circuit (ESD protection)



#### 3.2 Current sense output

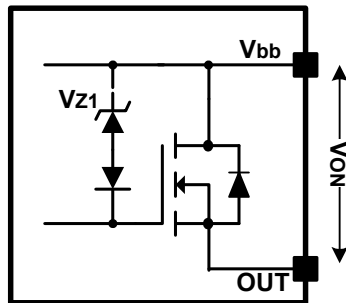


$V_{Z,IS} = 47.3V$  (typ.),  $R_{IS} = 1K\Omega$  nominal (or  $1K\Omega/n$ , if  $n$  devices are connected in parallel).  $I_S = I_L/k_{iis}$  can be only driven by the internal circuit as long as  $V_{OUT} - V_{IS} > 5V$ . Therefore  $R_{IS}$  should be less than

$$\frac{V_{bb} - 5V}{8.5mA}$$

Note: For large values of  $R_{IS}$  the voltage  $V_{IS}$  can reach almost  $V_{bb}$ . See also over voltage protection. If you don't use the current sense output in your application, you can leave it open.

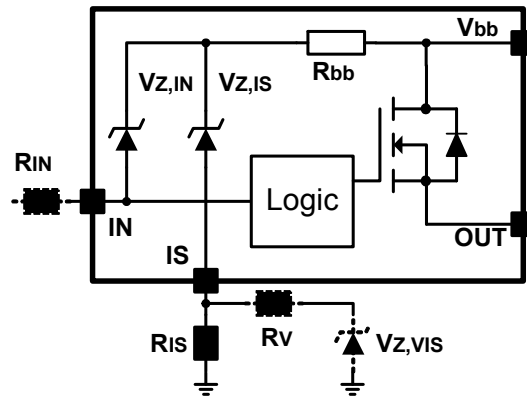
#### 3.3 Inductive and over voltage output clamp



$V_{ON}$  is clamped to  $V_{ON(CL)} = 41.5V$  typical

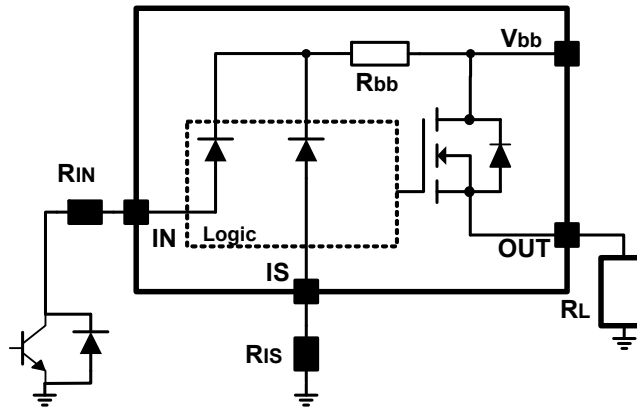


### 3.4 Overvoltage protection of logic part



$R_{bb} = 85\Omega$  typ.,  $V_{Z,IN} = V_{Z,IS} = 47.3V$  typ.,  $R_{IS} = 1K\Omega$  nominal. Note that when over voltage exceeds 47.3V typ. a voltage above 5V can occur between IS and GND, if  $R_V$ ,  $V_{Z,VIS}$  are not used.

### 3.5 Reverse battery protection

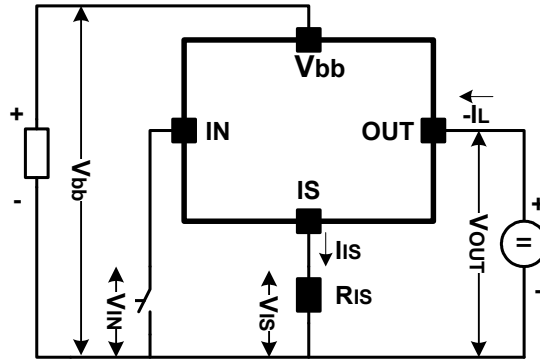


$R_{IS}$  typ.  $1K\Omega$ . Add  $R_{IN}$  for reverse battery protection in application with  $V_{bb}$  above 16V;

$$\frac{1}{R_{IN}} + \frac{1}{R_{IS}} > \frac{0.082A}{|V_{bb} - 9V|}$$

To minimize power dissipation at reverse battery operation, the overall current into the IN and IS pin should be about 82mA. The current can be provided by using a small signal diode D in parallel to the input switch, by using a MOSFET input switch or by properly adjusting the current through  $R_{IS}$ . Since the current via  $R_{bb}$  generates additional heat in the device, this has to be taken into account in the overall thermal consideration.

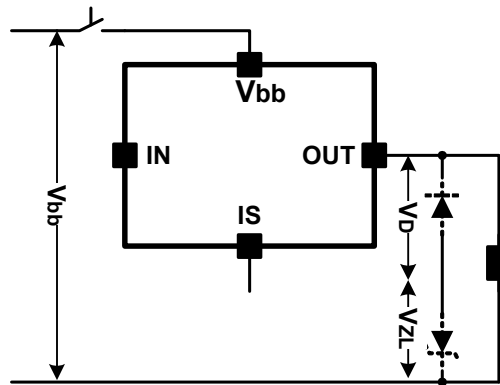
### 3.6 Inverse load current operation



The device can be operated in inverse load current mode ( $V_{OUT} > V_{bb} > 0V$ ). The current sense feature is not available during this kind of operation ( $I_{IS} = 0$ ). In case of inverse operation the intrinsic drain source diode is eventually conducting resulting in considerably increased power dissipation. The transition from inverse to forward mode can result in a delayed switch on.

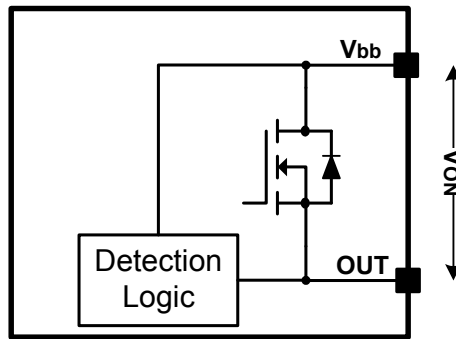
Note: Temperature protection during inverse load current operation is not possible!

### 3.7 Vbb disconnect with energized inductive load



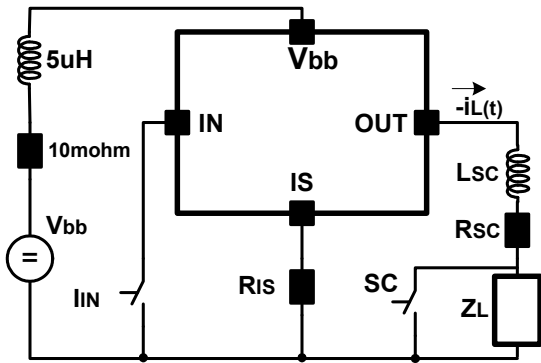
Provide a current path with load current capability by using a diode, a Z-diode, or a varistor ( $V_{ZL} + V_D < 38V$  if  $R_{IN} = 0$ ). For higher clamp voltages currents at IN and IS have to be limited to 120 mA.

### 3.8 Overload detection



Fault Condition:  $V_{ON} > 1V$  typ.

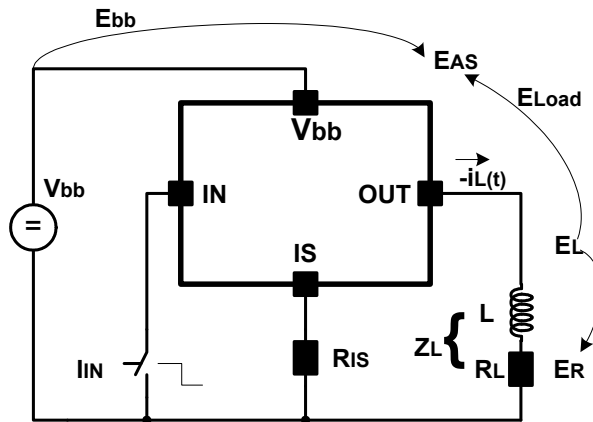
### 3.9 Short circuit



**Fault Condition:**  $V_{ON} > V_{ON(SC)}$  (3.5V typ.) and  $t > t_d(SC)$  (typ. 650us)

Short circuit is a combination of primary and secondary impedances and resistances.

### 3.10 Inductive load switch-off energy dissipation



Energy stored in load inductance:

$$E_L = 1/2 \cdot L \cdot I_L^2$$

While demagnetizing load inductance, the energy dissipated in MOSFET is

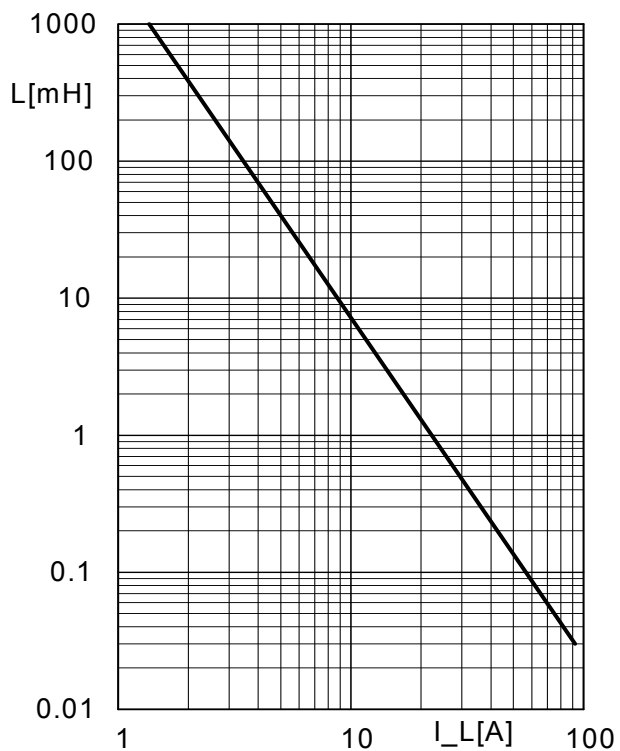
$$E_{AS} = E_{bb} + E_L - E_R = \int V_{ON(CL)} \cdot i_L(t) dt$$

with an approximate solution for  $R_L > 0$   $V_{out(CL)} = V_{ON(CL)} - V_{bb}$ :

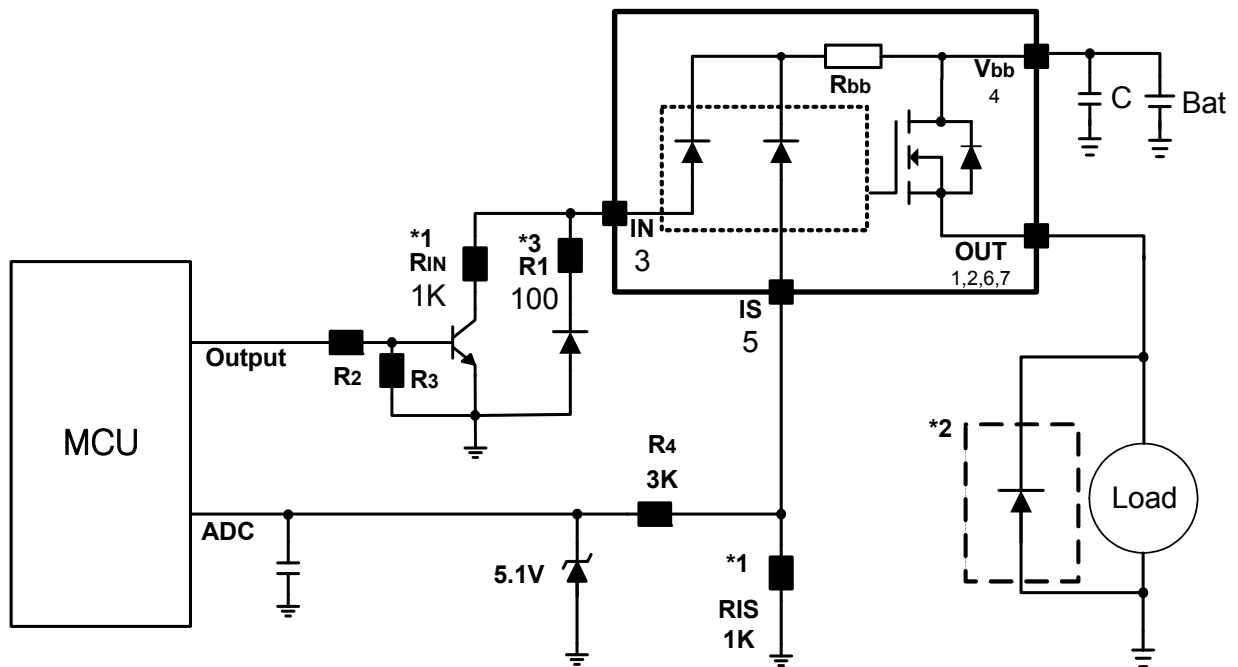
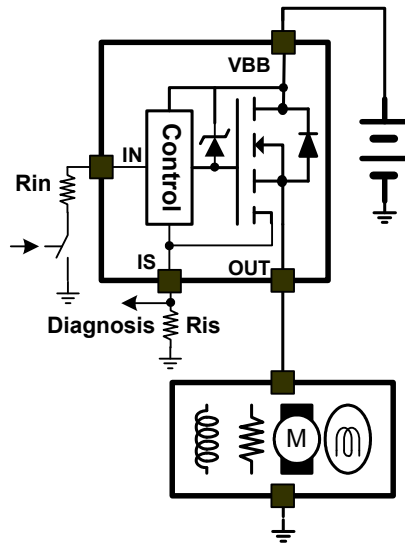
$$E_{AS} = \frac{I_L \cdot L}{2 \cdot R_L} (V_{ON(CL)}) \ln \left( 1 + \frac{I_L \cdot R_L}{|V_{OUT(CL)}|} \right)$$

### 3.11 Maximum allowable load inductance for a single switch off

$L=f(I_L)$ ;  $T_j = 150^\circ\text{C}$ ,  $V_{bb}=12\text{V}$ ,  $R_L = 0\Omega$

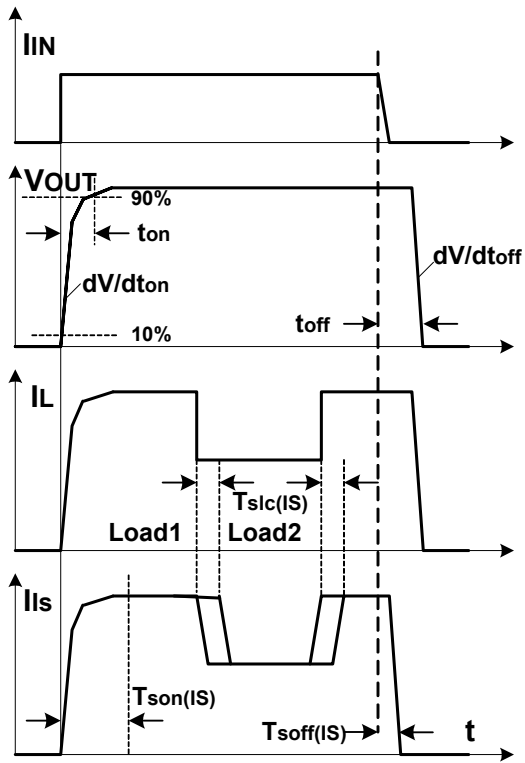


### Typical Application Circuit



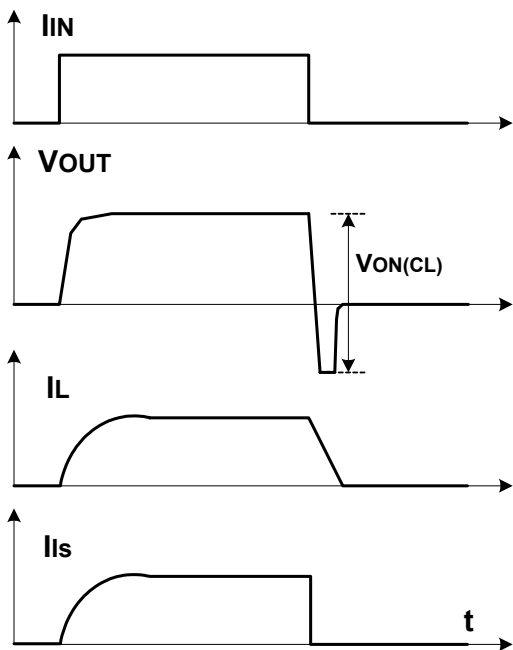
- 1) Ris and Rin is recommended as 1k
- 2) Put diode or capacitor between load to protect device or to remove noise.
- 3) For reverse battery protection function, R1 should be used less than 120Ω at -18V.

### Timing Diagrams

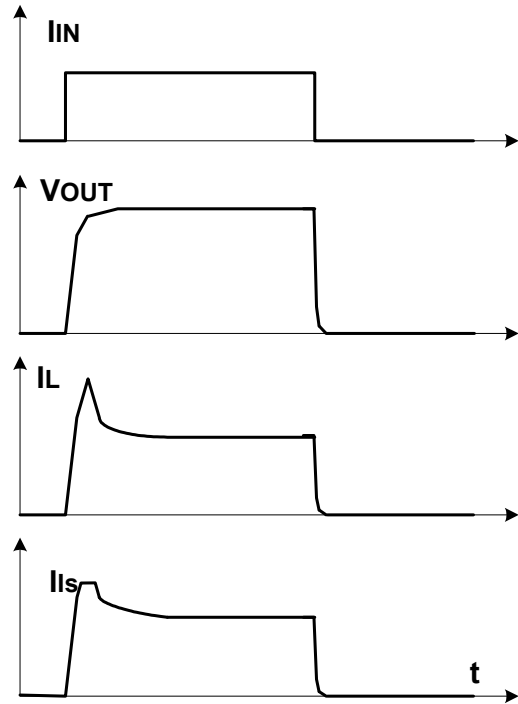


**Figure 1a. Switching a resistive load, change of load current in ON-condition**

The sense signal is not valid during a settling time after turn-on/off and after change of load current

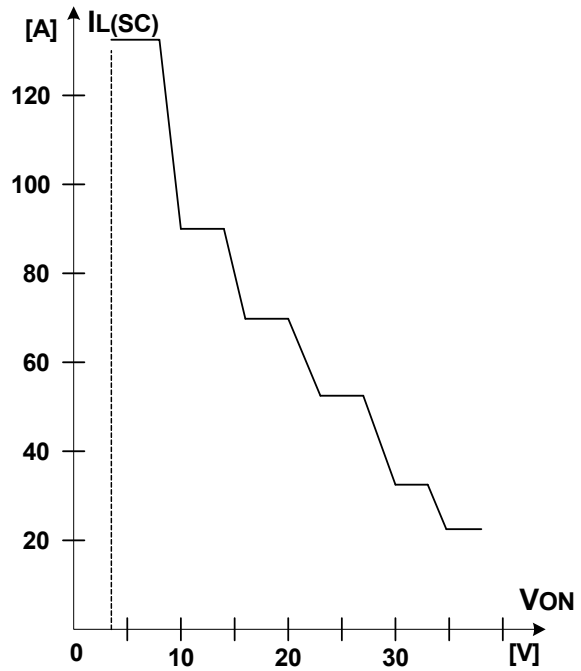


**Figure 2b. Switching an inductive load**



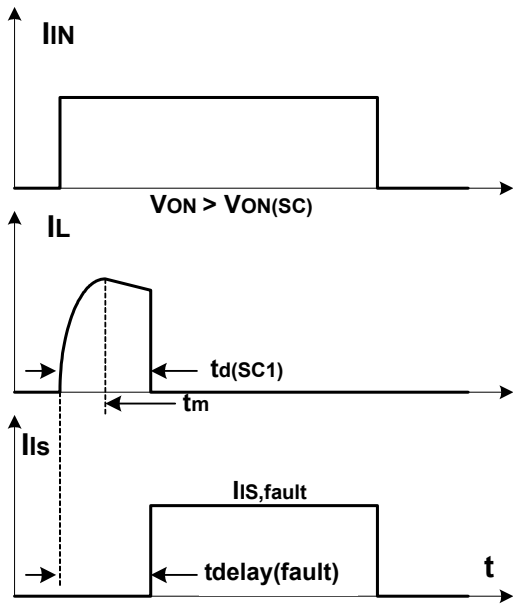
**Figure 2a. Switching motors and lamps**

As long as  $V_{bIS} < V_{Z,IS}$  the sense current will never exceed  $I_{IS,fault}$  and/or  $I_{IS,lim}$



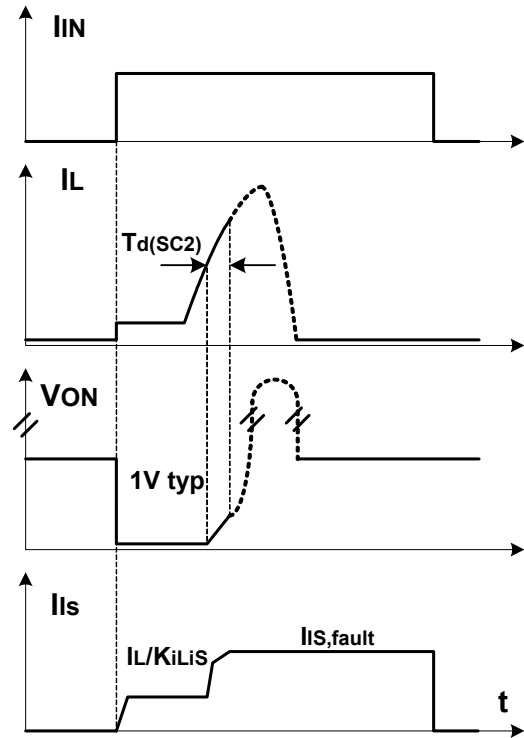
**Figure 3a. Typical current limitation characteristic**

In case of  $V_{ON} > V_{ON(SC)}$  (typ. 3.5V) the device will be switched off by internal short circuit detection

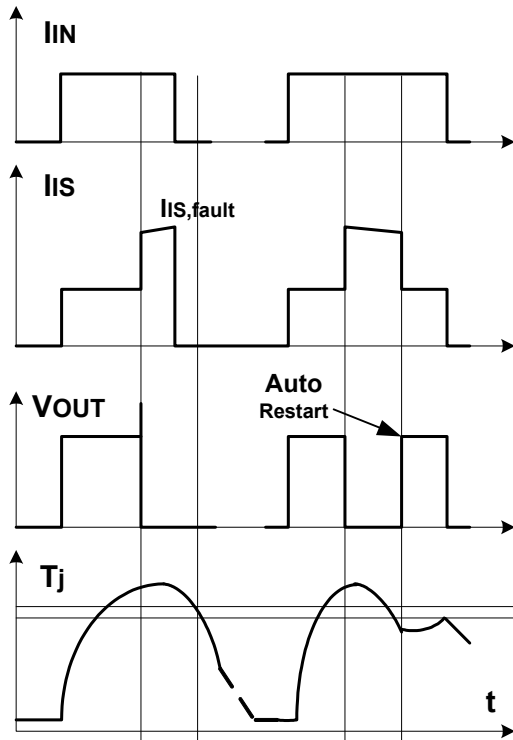


**Figure 3b. Short circuit type one:** shut down by short circuit detection, reset by IIN=0

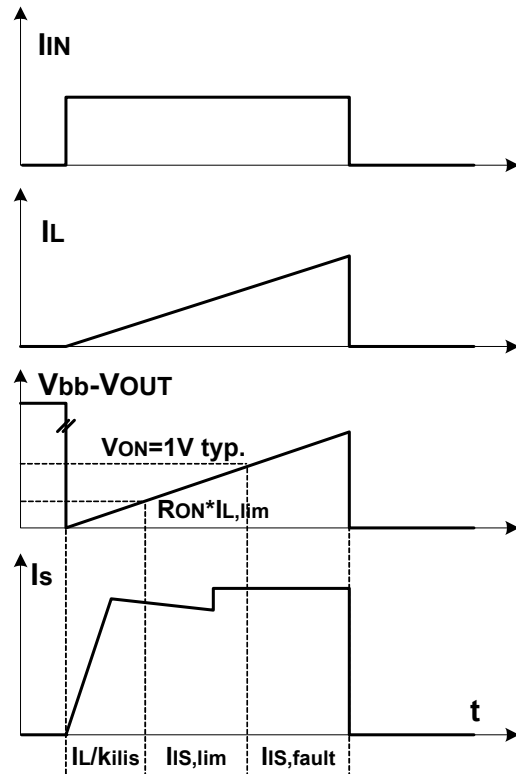
Shut down remains latched until next reset via input



**Figure 3c. Short circuit type two:** shut down by short circuit detection, reset by IIN=0



**Figure 4a. Over temperature Reset if  $T_j < T_{jt}$**



**Figure 4b. Overload Reset if  $T_j < T_{jt}$**

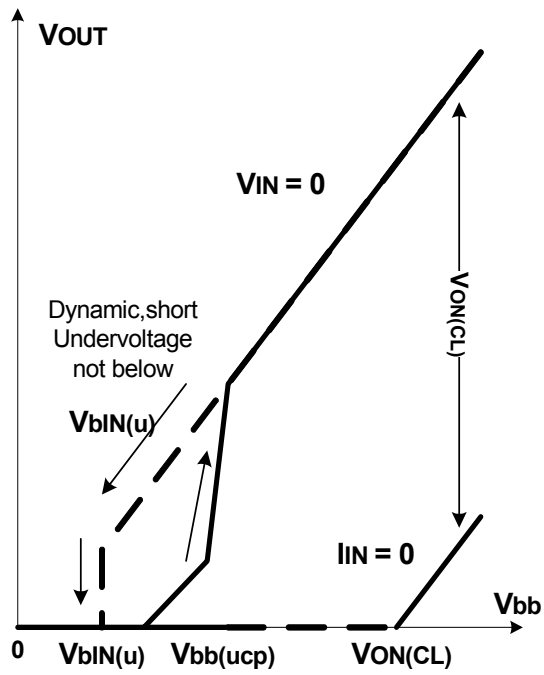


Figure 5. Under voltage restart of charge pump and over voltage clamp

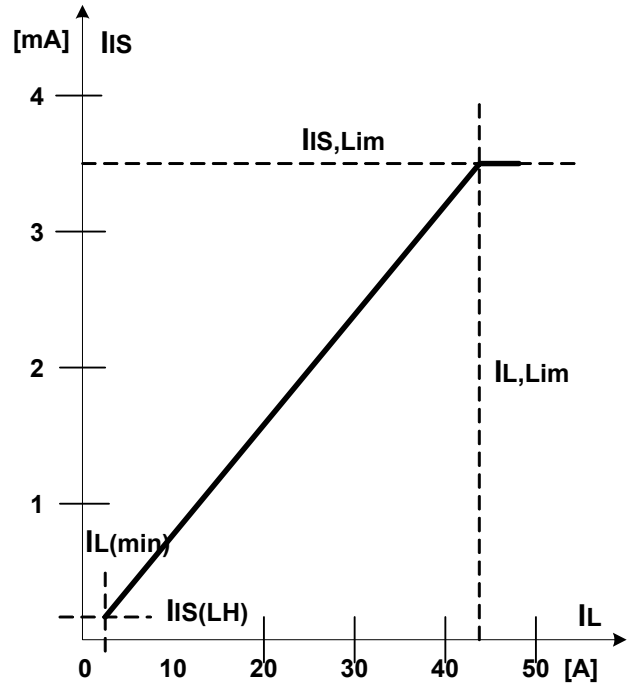


Figure 6a. Current sense versus load current

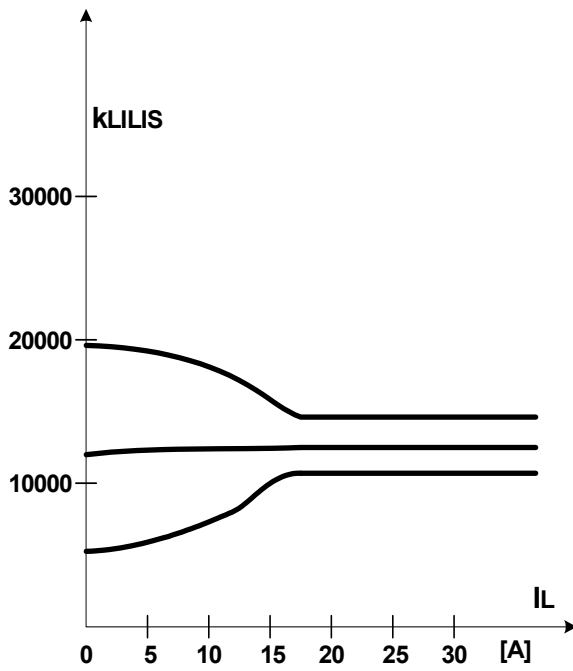


Figure 6b. Current sense ratio

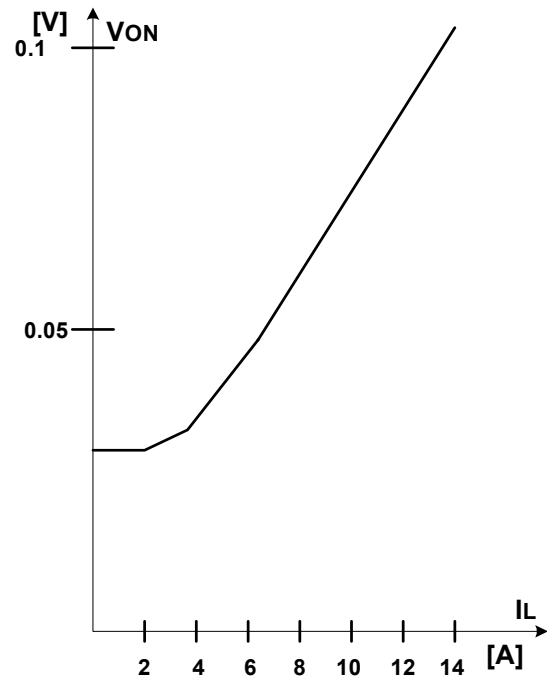
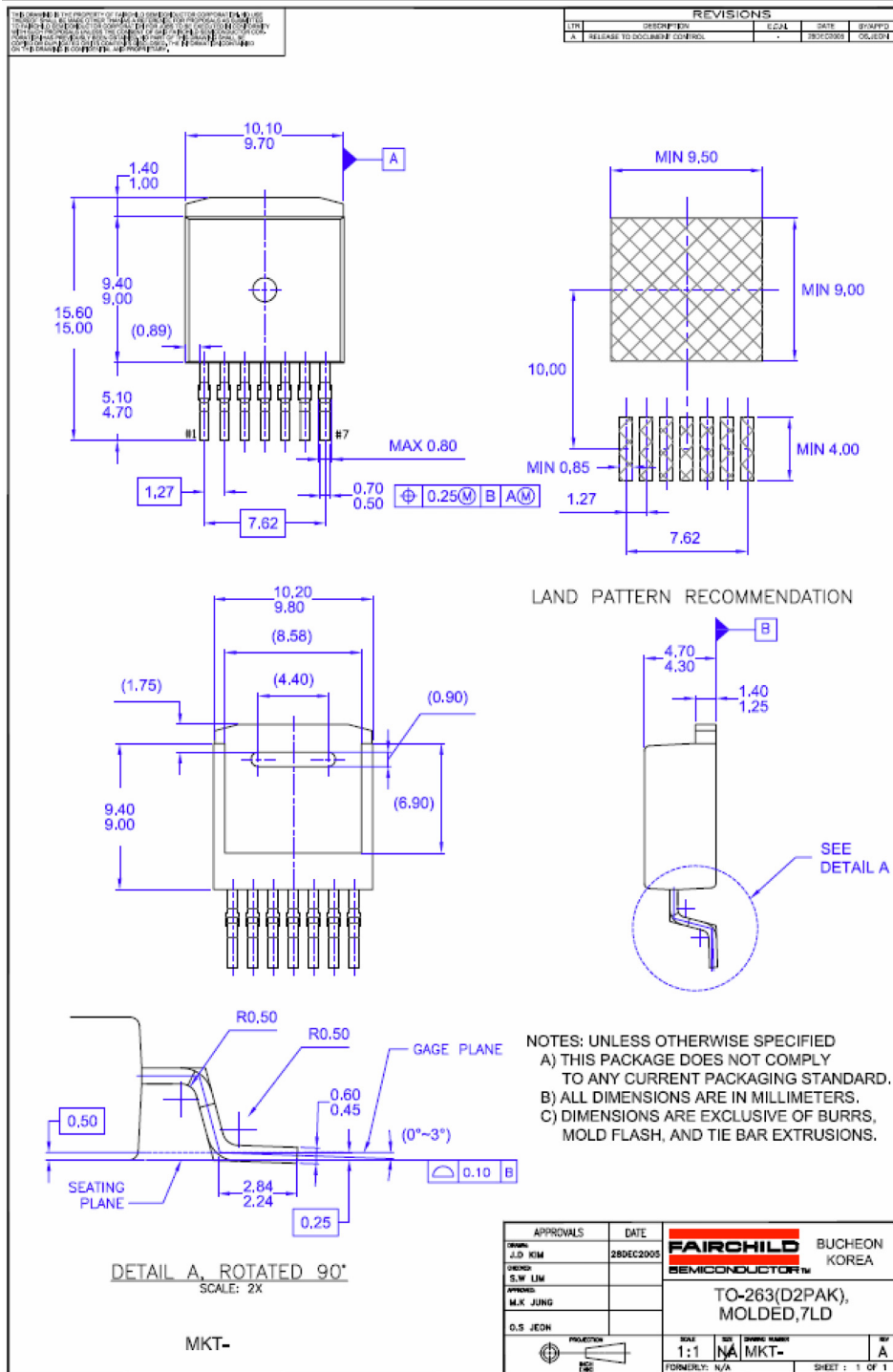
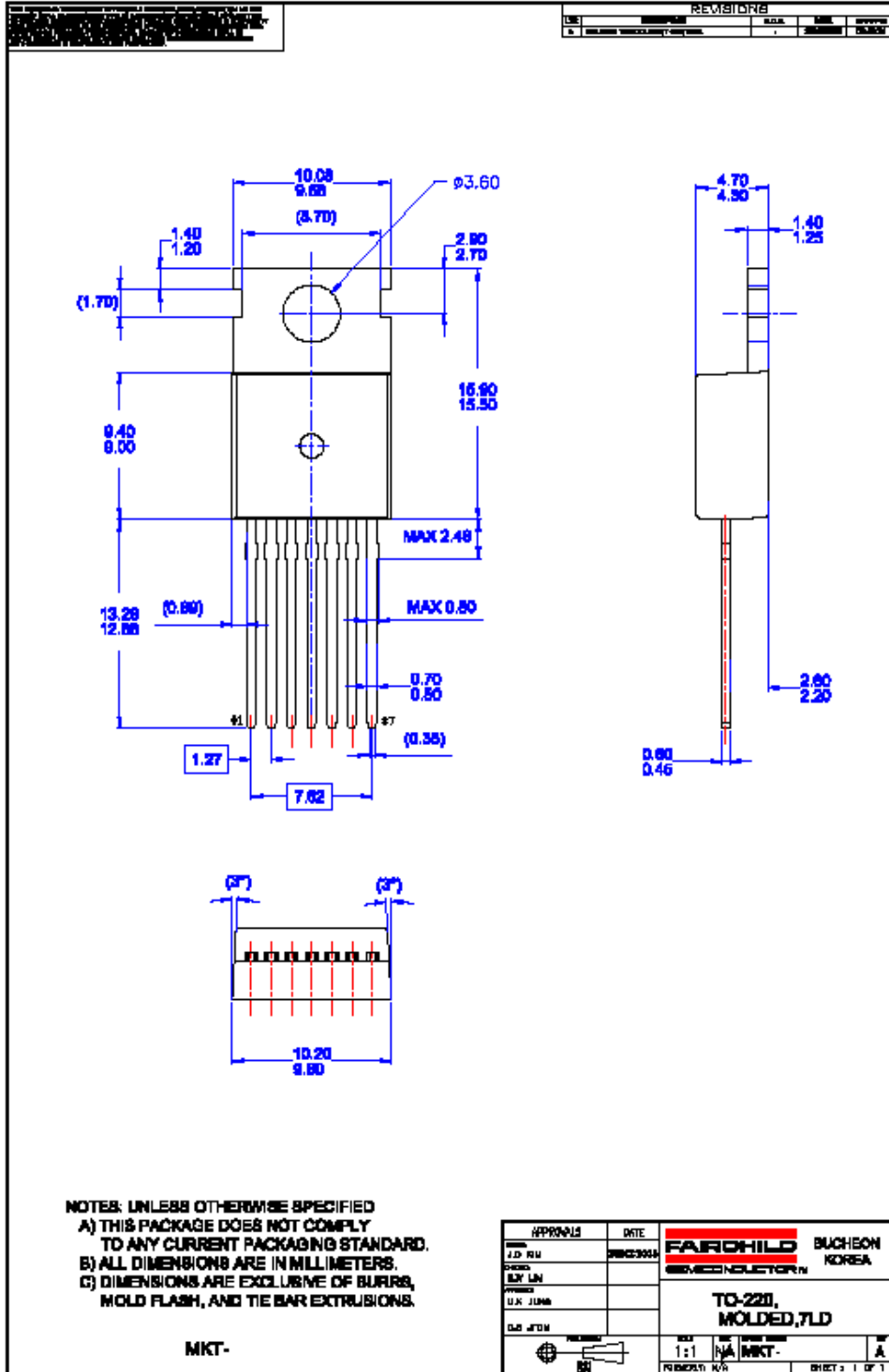


Figure 7. Output voltage drop versus load current



# Package Dimensions





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The Power Franchise™		OPTOPLANAR™	SPM™	
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